# SPECIFICATION
# FOR
# APPROVAL

( ● ) **Preliminary Specification**
( ) **Final Specification**

| Title | 13.3” WXGA TFT LCD |
|---|---|

| BUYER | HP |
|---|---|
| MODEL | |

| SUPPLIER | LG.Philips LCD Co., Ltd. |
|---|---|
| *MODEL | LP133WX2 |
| Suffix | TLE1 |

*When you obtain standard approval,
please use the above model name without suffix

| APPROVED BY | SIGNATURE |
|---|---|
| / | |
| / | |
| / | |

Please return 1 copy for your confirmation with your signature and comments.

| APPROVED BY | SIGNATURE |
|---|---|
| **K. J. KWON / S.Manager** | |
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# Contents

# RECORD OF REVISIONS

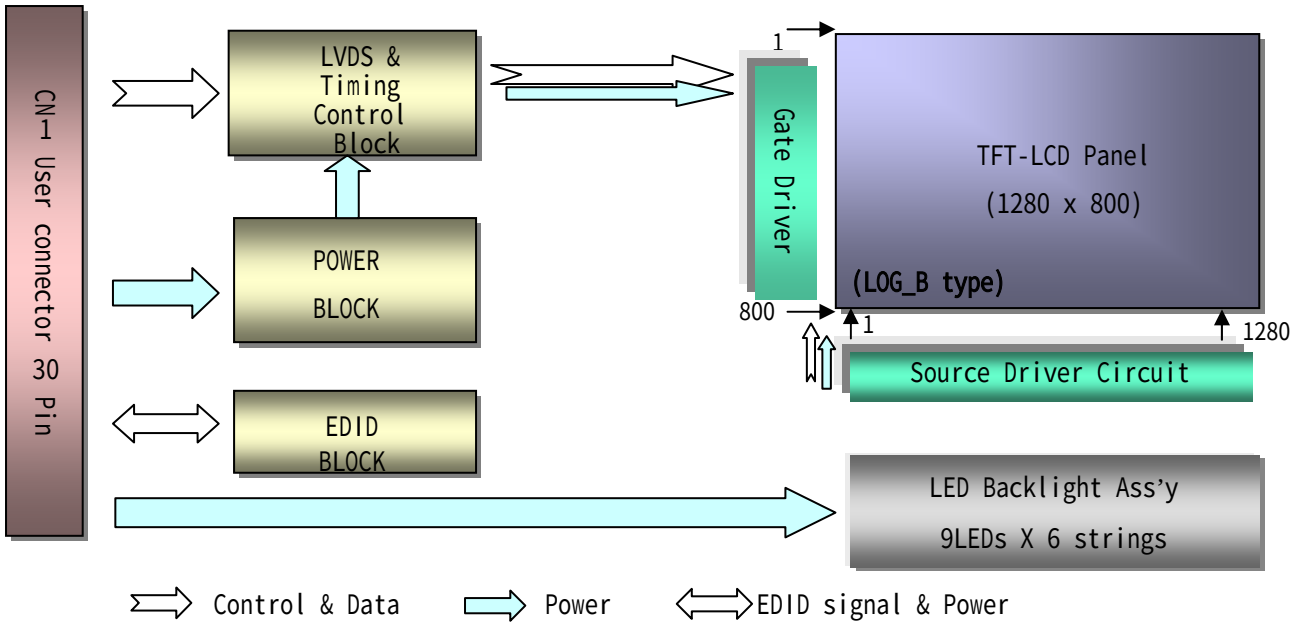
| Revision No | Revision Date | Page | Description | EDID ver |
|---|---|---|---|---|
| 0.0 | 19. Mar. 2008 | - | First Draft | - |
| 0.1 | 08.May.2008 | 6 | Module Connector Pin Configuration Update | - |
| 0.2 | 15.May.2008 | 6 | User Connector Configuration Image Update | - |
| 0.3 | 3. Jun.,2008 | 15,16 | LCM drawing Update | 0.2 |
| 0.4 | 20. Jun.2008 | 5,11,12 | Optical spec update (life time, luminance, luminance variation) | 0.3 |
| | | 8 | Timing spec update (Dclk 71Mhz → 69.3Mhz) | |
| | | 22,23,24 | EDID update (Timing spec change) | |

## 1. General Description

The LP133WX2 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 13.3 inches diagonally measured active display area with WXGA resolution(1280 horizontal by 800 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP133WX2 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP133WX2 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP133WX2 characteristics provide an excellent flat display for office automation products such as Notebook PC.




## General Features

| | |
|---|---|
| Active Screen Size | 13.3 inches diagonal |
| Outline Dimension | 296.0(H) × 203(V) × 3.5(D, Max.) mm |
| Pixel Pitch | 0.2235 mm × 0.2235 mm |
| Pixel Format | 1280 horiz. by 800 vert. Pixels RGB strip arrangement |
| Color Depth | 6-bit, 262,144 colors |
| Luminance, White | 200 cd/m$^2$(Typ., @$I_{LED}$=19mA) |
| Power Consumption | Logic : 0.9 W (typ.@Mosaic), Back Light : TBD W (typ.@ $I_{LED}$= 19mA) |
| Weight | 275g(Typ.), 290(Max.) |
| Display Operating Mode | Transmissive mode, normally white |
| Surface Treatment | Hard coating(3H), Glare treatment of the front Polarizer (Haze 0%) |

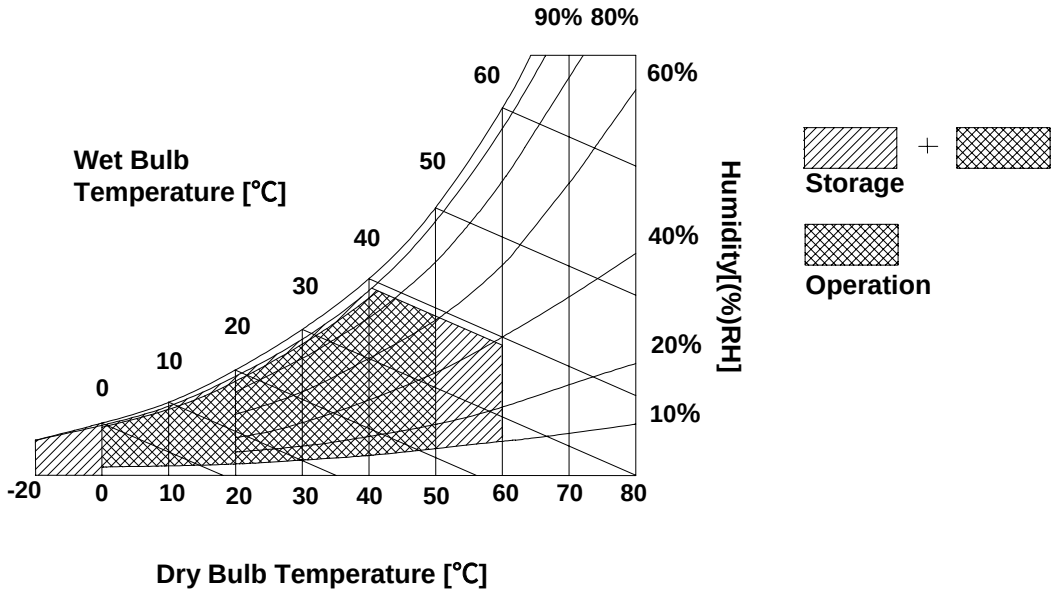
## 2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

**Table 1. ABSOLUTE MAXIMUM RATINGS**

| Parameter | Symbol | Values | | Units | Notes |
|---|---|---|---|---|---|
| | | Min | Max | | |
| Power Input Voltage | VCC | -0.3 | 4.0 | Vdc | at 25 ± 5°C |
| Operating Temperature | TOP | 0 | 50 | °C | 1 |
| Storage Temperature | HST | -20 | 60 | °C | 1 |
| Operating Ambient Humidity | HOP | 10 | 90 | %RH | 1 |
| Storage Humidity | HST | 10 | 90 | %RH | 1 |

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39°C Max, and no condensation of water.



# 3. Electrical Specifications

## 3-1. Electrical Characteristics

The LP133WX2 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

**Table 2. ELECTRICAL CHARACTERISTICS**

| Parameter | Symbol | Values | | | Unit | Notes |
|---|---|---|---|---|---|---|
| | | Min | Typ | Max | | |
| MODULE : | | | | | | |
| Power Supply Input Voltage | VCC | 3.0 | 3.3 | 3.6 | $V_{DC}$ | |
| Power Supply Input Current | $I_{CC}$ | 230 | 273 | 313 | mA | 1 |
| Power Consumption | Pc | | 0.9 | | Watt | 1 |
| Differential Impedance | Zm | 90 | 100 | 110 | Ohm | 2 |
| LED Backlight : | | | | | | |
| Operating Voltage | $I_{LED}$ | - | - | - | V | 3 |
| Operating Current per string | $I_{LED}$ | | 19 | | mA | 3 |
| Power Consumption | $P_{BL}$ | - | TBD | | Watt | 4 |
| Life Time | | 10,000 | - | - | Hrs | 5 |
| PWM Input Signal | | | | | | |
| Operating Frequency (for Operating) | | 200 | | 1500 | Hz | 6 |
| Operating Frequency (for Reliability) | | 206 | 210 | 215 | Hz | |
| On Duty | | 2 | | 100 | % | 7 |
| On Time | | 50 | | | us | |
| Maximum Voltage | | | | 5 | V | |
| On threshold | | 2.1 | | | V | |
| Off threshold | | | | 0.8 | V | |
| LED Current | | | | | | |
| High State | | - | 19 | - | mA | |
| Low State | | - | 0 | - | mA | |

Note)

1. The specified current and power consumption are under the Vcc = 3.3V , 25°C, fv = 60Hz condition whereas mosaic pattern is displayed and fv is the frame frequency.
2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
3. The typical operating current is for the typical surface luminance ($L_{WH}$) in optical characteristics. $I_{LED}$ is the current of each LEDs' string, LED backlight has 6 strings on it.
4. The LED power consumption shown above does not include power of external LED driver circuit for typical current condition.
5. The life time is determined as the time at which brightness of LED is 50% compare to that of initial value at the typical LED current.
6. LED Driver operating Frequency
7. There may be a flickering Under 6% dimming.

## 3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.
The electronics interface connector is a model I-PEX 20347-340E manufactured by I-PEX.

**Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)**

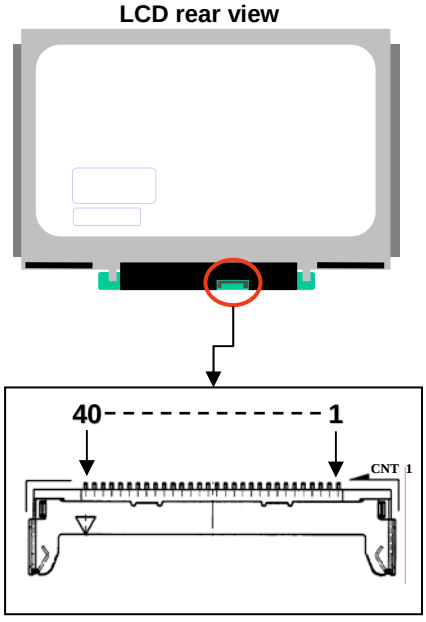
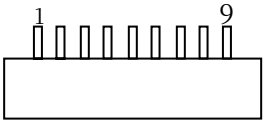
| Pin | Symbol | Description | Notes |
|---|---|---|---|
| 1 | BIST/CT1 | BIST/Connector Test | [LVDS Receiver]<br>Silicon Works<br><br>[Connector]<br>I-PEX 20347-340E-12<br>[Mating Connector]<br>I-PEX 20345-#40E-## series<br>or equivalent<br><br>[Connector pin arrangement]<br><br>LCD rear view<br>  <br> |
| 2 | VDD | Power Supply (3.3V typ.) | |
| 3 | VDD | Power Supply (3.3V typ.) | |
| 4 | $V_{EDID}$ | DDC 3.3V power | |
| 5 | $CLK_{EDID}$ | DDC clock / SMBus clock | |
| 6 | $DATA_{EDID}$ | DDC data / SMBus data | |
| 7 | Rin0- | - LVDS differential data input (R0-R5,G0) | |
| 8 | Rin0+ | + LVDS differential data input (R0-R5,G0) | |
| 9 | VSS | Ground | |
| 10 | Rin1- | - LVDS differential data input (G1-G5,B0-B1) | |
| 11 | Rin1+ | + LVDS differential data input (G1-G5,B0-B1) | |
| 12 | VSS | Ground | |
| 13 | Rin2- | - LVDS differential data input (B2-B5,HS,VS,DE) | |
| 14 | Rin2+ | + LVDS differential data input (B2-B5,HS,VS,DE) | |
| 15 | VSS | Ground | |
| 16 | ClkIN- | - LVDS differential clock input | |
| 17 | ClkIN+ | + LVDS differential clock input | |
| 18 | VSS | Ground | |
| 19 | NC | No Connection | |
| 20 | NC | No Connection | |
| 21 | NC | No Connection | |
| 22 | NC | No Connection | |
| 23 | NC | No Connection | |
| 24 | NC | No Connection | |
| 25 | NC | No Connection | |
| 26 | NC | No Connection | |
| 27 | NC | No Connection | |
| 28 | NC | No Connection | |
| 29 | NC | No Connection | |
| 30 | VBL- | LED power return | |
| 31 | VBL- | LED power return | |
| 32 | VBL- | LED power return | |
| 33 | NC | No Connection | |
| 34 | BLIM | PWM for luminance control | |
| 35 | BL_EN | BL On/Off | |
| 36 | NC | No Connection | |
| 37 | VBL+ | 6V-20V LED power | |
| 38 | VBL+ | 6V-20V LED power | |
| 39 | VBL+ | 6V-20V LED power | |
| 40 | BIST/CT2 | BIST/Connector Test | |

**Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION (CN2)**

The LED backlight connector is a model TF12-9S-0.5H, manufactured by Hirose.

| Pin | Symbol | Description | Notes |
|---|---|---|---|
| 1 | Vdc1 | LED Cathode (Negative) |   |
| 2 | Vdc2 | LED Cathode (Negative) | |
| 3 | Vdc3 | LED Cathode (Negative) | |
| 4 | Vdc4 | LED Cathode (Negative) | |
| 5 | Vdc5 | LED Cathode (Negative) | |
| 6 | Vdc6 | LED Cathode (Negative) | |
| 7 | NC | No Connection | |
| 8 | Vdc(1,2,3,4,5,6) | LED Anode(Positive) | |
| 9 | Vdc(1,2,3,4,5,6) | LED Anode(Positive) | |

## 3-3. Signal Timing Specifications

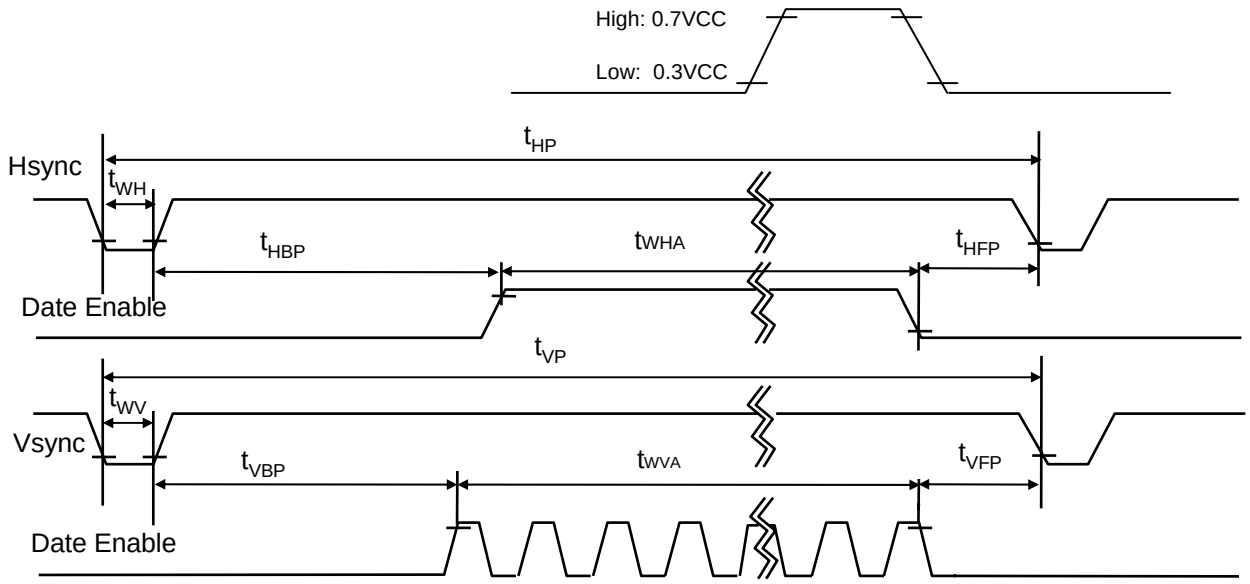
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

**Table 5. TIMING TABLE**

| ITEM | Symbol | | Min. | Typ. | Max. | Unit | Note |
|---|---|---|---|---|---|---|---|
| DCLK | Frequency | $f_{CLK}$ | 66.4 | 69.4 | 72.4 | MHz | |
| Hsync | Active | $tw_{HA}$ | 1280 | 1280 | 1280 | tCLK | |
| | Period | $t_{HP}$ | 1408 | 1416 | 1460 | | |
| | Width-Active | $t_{WH}$ | 32 | 32 | 64 | | |
| Vsync | Active | $tw_{VA}$ | 800 | 800 | 800 | tHP | |
| | Period | $t_{VP}$ | 811 | 816 | 847 | | |
| | Width-Active | $t_{WV}$ | 4 | 6 | 9 | | |
| Data Enable | Horizontal back porch | $t_{HBP}$ | 48 | 56 | 80 | tCLK | |
| | Horizontal front porch | $t_{HFP}$ | 48 | 48 | 80 | | |
| | Vertical back porch | $t_{VBP}$ | 5 | 6 | 35 | tHP | |
| | Vertical front porch | $t_{VFP}$ | 2 | 2 | 5 | | |

## 3-4. Signal Timing Waveforms

Condition : $V_{CC}$ =3.3V



## 3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

**Table 6. COLOR DATA REFERENCE**

| Color | | Input Color Data | | | | | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | | RED | | | | | | GREEN | | | | | | BLUE | | | | | |
| | | MSB | | | | | LSB | MSB | | | | | LSB | MSB | | | | | LSB |
| | | R5 | R4 | R3 | R2 | R1 | R0 | G5 | G4 | G3 | G2 | G1 | G0 | B5 | B4 | B3 | B2 | B1 | B0 |
| Basic Color | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| RED | RED (00) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | RED (01) | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | … | | | … | | | | | | … | | | | | | … | | | |
| | RED (62) | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | RED (63) | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| GREEN | GREEN (00) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | GREEN (01) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | ... | | | ... | | | | | | ... | | | | | | ... | | | |
| | GREEN (62) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | GREEN (63) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| BLUE | BLUE (00) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | BLUE (01) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| | … | | | … | | | | | | … | | | | | | … | | | |
| | BLUE (62) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
| | BLUE (63) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |

## 3-6. Power Sequence

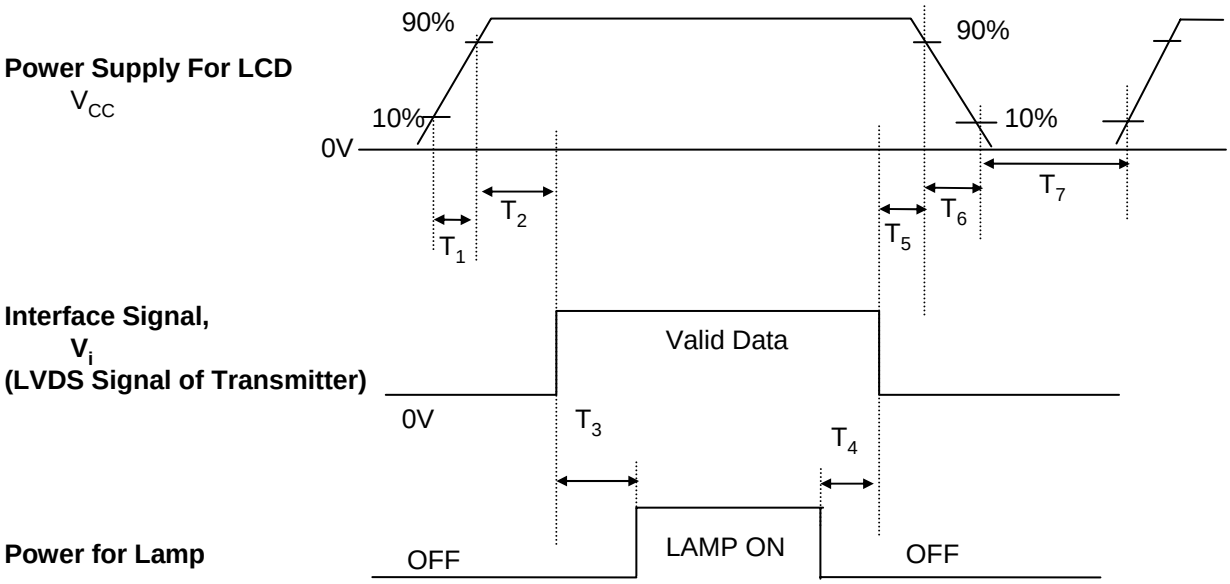



**Table 7. POWER SEQUENCE TABLE**

| Parameter | Value | | | Units |
|---|---|---|---|---|
| | Min. | Typ. | Max. | |
| $T_1$ | - | - | 10 | (ms) |
| $T_2$ | 0 | - | 50 | (ms) |
| $T_3$ | 200 | - | - | (ms) |
| $T_4$ | 200 | - | - | (ms) |
| $T_5$ | 0 | - | 50 | (ms) |
| $T_6$ | 0 | - | 10 | (ms) |
| $T_7$ | 400 | - | - | (ms) |

Note)
1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

## 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.
FIG. 1 presents additional information concerning the measurement equipment and method.

**FIG. 1 Optical Characteristic Measurement Equipment and Method**

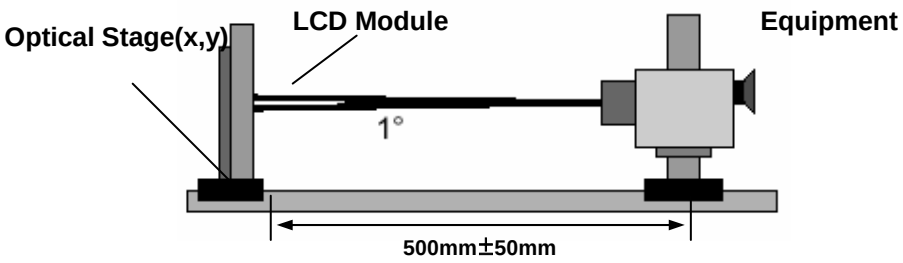



**Table 8. OPTICAL CHARACTERISTICS**

Ta=25°C, VCC=3.3V, fv=60Hz, $f_{CLK}$= 71.0MHz, ILED =19mA

| Parameter | Symbol | Values | | | Units | Notes |
|---|---|---|---|---|---|---|
| | | Min | Typ | MAx | | |
| Contrast Ratio | CR | 350 | 500 | - | | 1 |
| Surface Luminance, white | $L_{WH}$ | 170 | 200 | - | cd/m$^2$ | 2 |
| Luminance Variation | $\delta_{WHITE}$ | | 1.4 | 1.6 | % | 3 |
| Response Time | $Tr_R + Tr_D$ | - | 16 | 25 | ms | 4 |
| Color Coordinates | | | | | | |
| RED | RX | TBD | TBD | TBD | | |
| | RY | TBD | TBD | TBD | | |
| GREEN | GX | TBD | TBD | TBD | | |
| | GY | TBD | TBD | TBD | | |
| BLUE | BX | TBD | TBD | TBD | | |
| | BY | TBD | TBD | TBD | | |
| WHITE | WX | 0.283 | 0.313 | 0.343 | | |
| | WY | 0.299 | 0.329 | 0.359 | | |
| Viewing Angle | | | | | | 5 |
| x axis, right(Φ=0°) | Θr | 30 | | | degree | |
| x axis, left (Φ=180°) | Θl | 30 | | | degree | |
| y axis, up (Φ=90°) | Θu | 10 | | | degree | |
| y axis, down (Φ=270°) | Θd | 20 | | | degree | |
| Gray Scale | - | - | | | | 6 |

Note)

1. Contrast Ratio(CR) is defined mathematically as

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = \text{Average}(L_1, L_2, \ldots L_5)$$

3. The variation in surface luminance , The panel total variation ($\delta_{WHITE}$) is determined by measuring $L_N$ at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{WHITE} = \frac{\text{Maximum}(L_1, L_2, \ldots L_{13})}{\text{Minimum}(L_1, L_2, \ldots L_{13})}$$

4. Response time is the time required for the display to transition from white to black (rise time, $Tr_R$) and from black to white(Decay Time, $Tr_D$). For additional information see FIG 3.

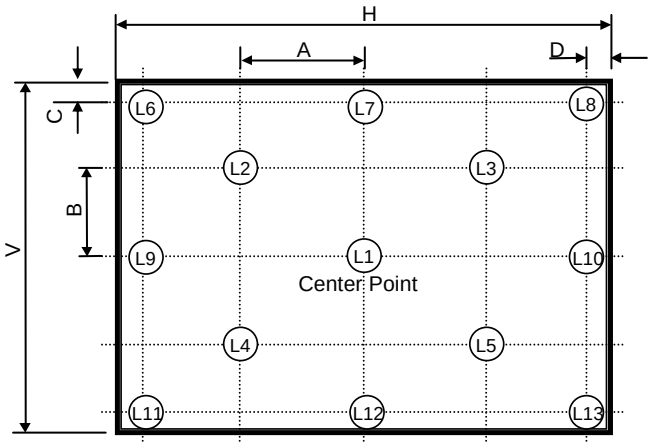
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6. Gray scale specification * $f_V$ = 60Hz

| Gray Level | Luminance [%] (Typ) |
|---|---|
| L0 | TBD |
| L7 | TBD |
| L15 | TBD |
| L23 | TBD |
| L31 | TBD |
| L39 | TBD |
| L47 | TBD |
| L55 | TBD |
| L63 | 100 |

**FIG. 2 Luminance**

<Measuring point for Average Luminance & measuring point for Luminance variation>




H,V : ACTIVE AREA
A : H/4 mm
B : V/4 mm
C : 10 mm
D : 10 mm
POINTS : 13 POINTS

**FIG. 3 Response Time**

The response time is defined as the following figure and shall be measured by switching the input signal for “black” and “white”.

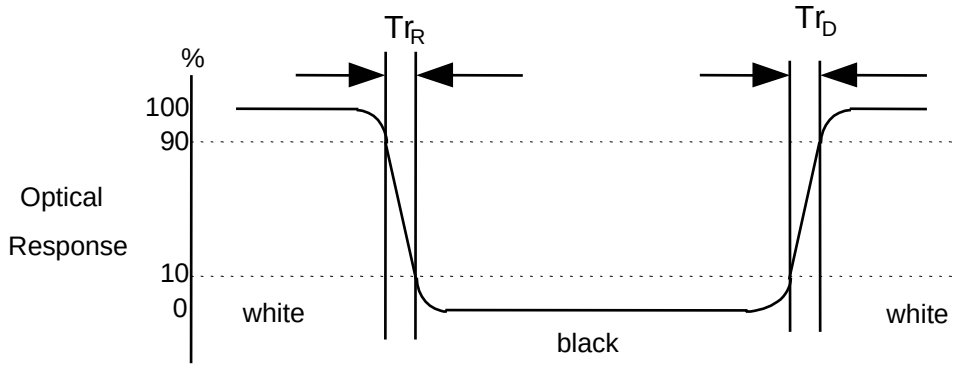
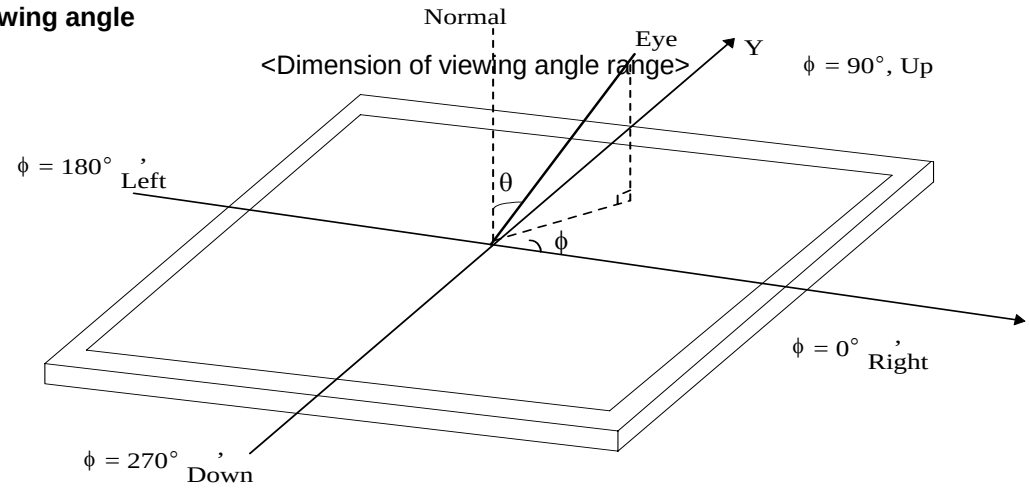



**FIG. 4 Viewing angle**



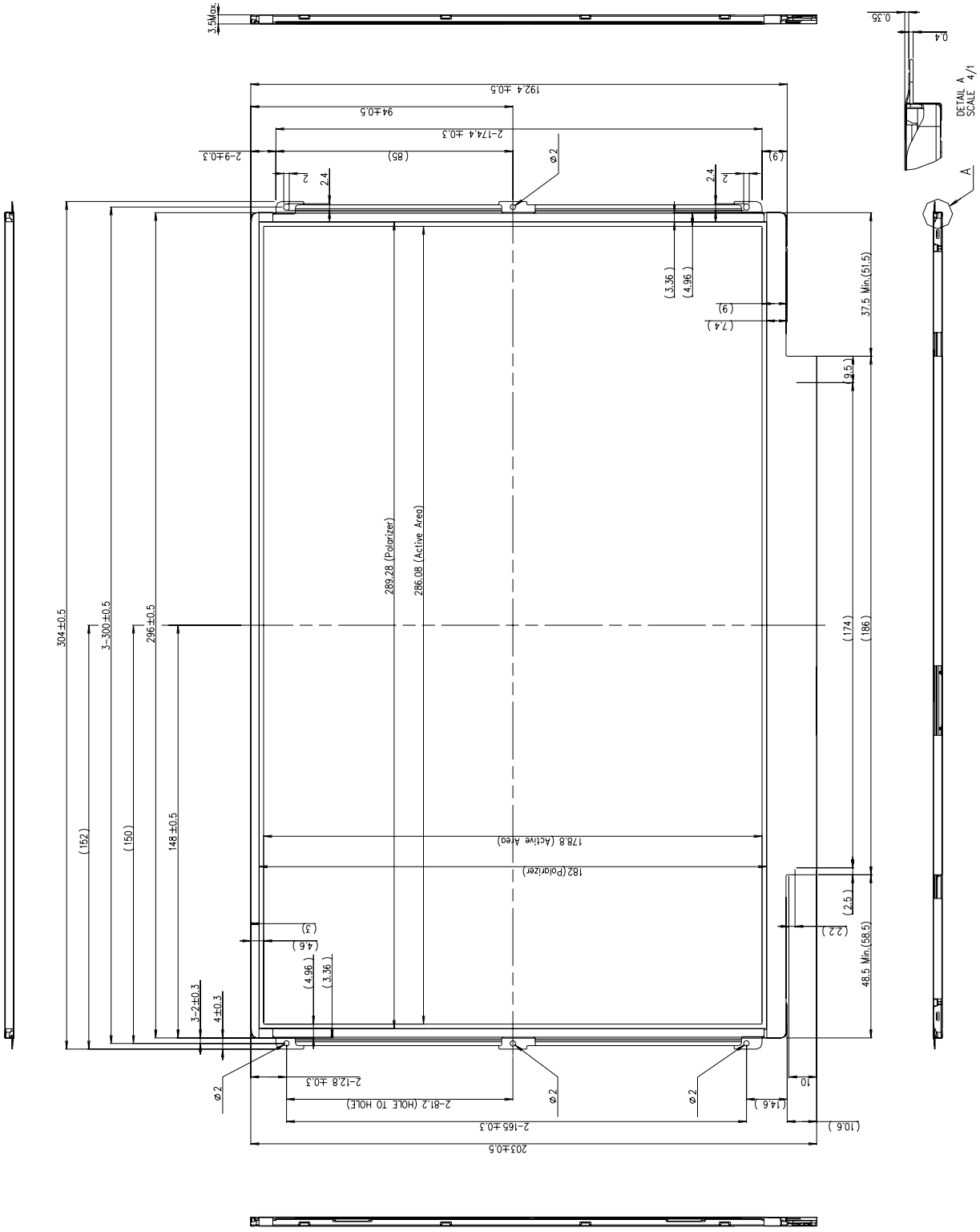
## 5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP133WX2.
In addition the figures in the next page are detailed mechanical drawing of the LCD.

| | | |
|---|---|---|
| Outline Dimension | Horizontal | 296.0 ± 0.50mm |
| | Vertical | 203 ± 0.50mm |
| | Depth | 3.5mm(Max.) |
| Bezel Area | Horizontal | TBD mm |
| | Vertical | TBD mm |
| Active Display Area | Horizontal | 286.08mm |
| | Vertical | 178.80 mm |
| Weight | 275g(Typ.), 290g (Max.) | |
| Surface Treatment | Hard coating(3H), Glare treatment of the front Polarizer (Haze 0%) | |

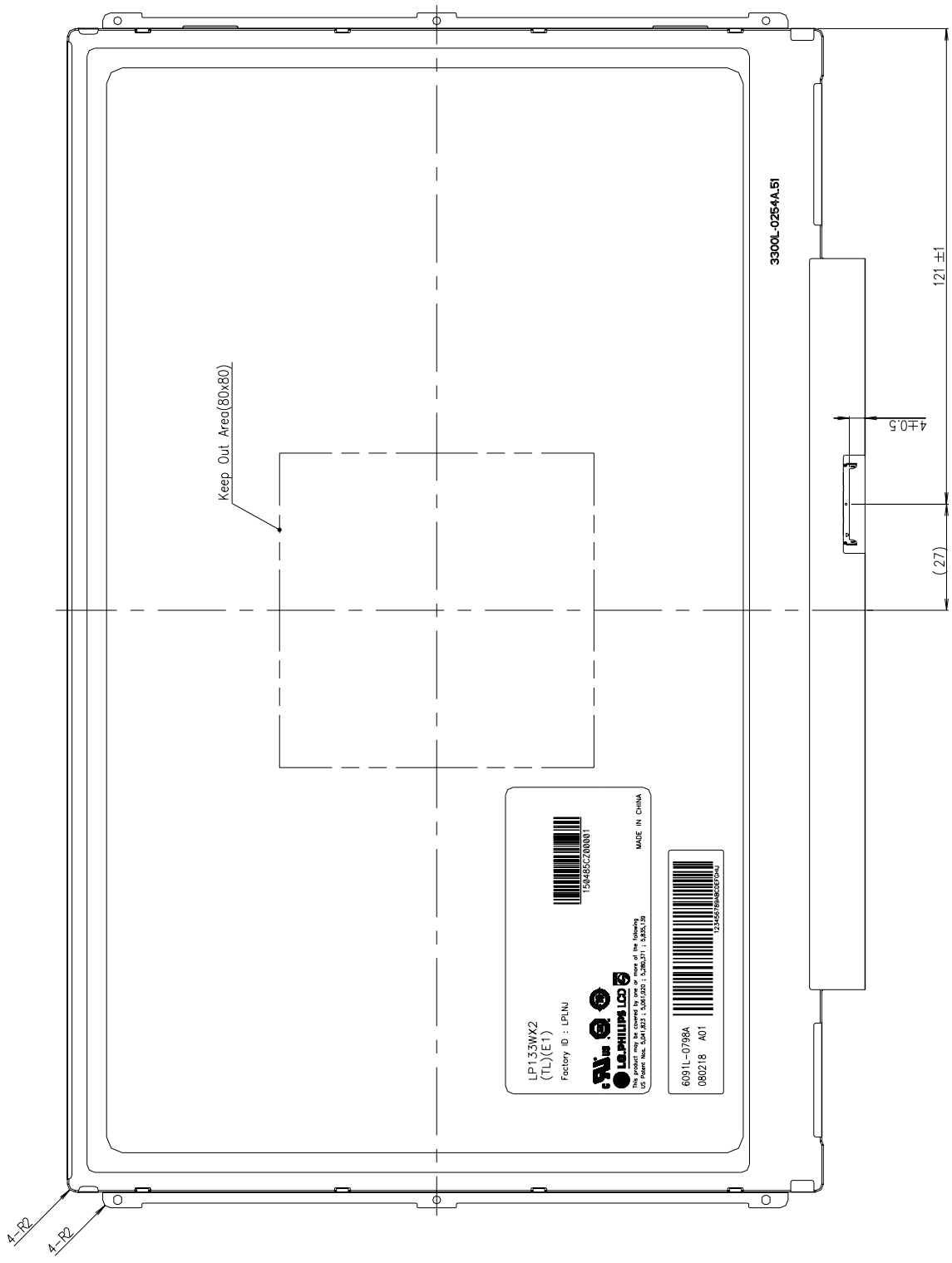
<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm



<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm



## 6. Reliability

Environment test condition

| No. | Test Item | Conditions |
|---|---|---|
| 1 | High temperature storage test | Ta= 60°C, 240h |
| 2 | Low temperature storage test | Ta= -20°C, 240h |
| 3 | High temperature operation test | Ta= 50°C, 50%RH, 240h |
| 4 | Low temperature operation test | Ta= 0°C, 240h |
| 5 | Vibration test (non-operating) | Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min<br>3 axis, 30min/axis |
| 6 | Shock test (non-operating) | - No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module<br>- No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays |
| 7 | Altitude operating<br>storage / shipment | 0 ~ 10,000 feet (3,048m) 24Hr<br>0 ~ 40,000 feet (12,192m) 24Hr |

{ Result Evaluation Criteria }
There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

# 7. International Standards

## 7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,
Standard for Safety of Information Technology Equipment.

## 7-2. EMC

a) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. “American National Standards Institute(ANSI), 1992
b) CISPR22 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.“ International Special Committee on Radio Interference.
c) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.“ European Committee for Electrotechnical Standardization.(CENELEC), 1998
( Including A1: 2000 )

# 8. Packing

## 8-1. Designation of Lot Mark

a) Lot Mark

| A | B | C | D | E | F | G | H | I | J | K | L | M |
|---|---|---|---|---|---|---|---|---|---|---|---|---|

A,B,C : SIZE(INCH)
D : YEAR
E : MONTH
F ~ M : SERIAL NO.

Note

1. YEAR

| Year | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 |
|---|---|---|---|---|---|---|---|---|---|---|
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 |

2. MONTH

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C |

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

## 8-2. Packing Form

a) Package quantity in one box : 20 pcs

b) Box Size : 482mm × 278mm × 383mm

# 9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

## 9-1. MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

## 9-2. OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V=± 200mV(Over and under shoot voltage)

(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

## 9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

## 9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

## 9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

## 9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
Please carefully peel off the protection film without rubbing it against the polarizer.
(3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
(4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

## APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

| Byte# (decimal) | Byte# (HEX) | Field Name and Comments | Value (HEX) | | Value (binary) | |
|---|---|---|---|---|---|---|
| 0 | 00 | Header | 0 | 0 | 0000 0000 | Header |
| 1 | 01 | Header | F | F | 1111 1111 | |
| 2 | 02 | Header | F | F | 1111 1111 | |
| 3 | 03 | Header | F | F | 1111 1111 | |
| 4 | 04 | Header | F | F | 1111 1111 | |
| 5 | 05 | Header | F | F | 1111 1111 | |
| 6 | 06 | Header | F | F | 1111 1111 | |
| 7 | 07 | Header | 0 | 0 | 0000 0000 | |
| 8 | 08 | EISA manufacturer code(3 Character ID) = LGD | 3 | 0 | 0011 0000 | Vender/ Product ID |
| 9 | 09 | Compressed ASCII | E | 4 | 1110 0100 | |
| 10 | 0A | Panel Supplier Reserved - Product code | 6 | 8 | 0110 1000 | |
| 11 | 0B | (Hex, LSB first) | 0 | 1 | 0000 0001 | |
| 12 | 0C | LCD Module Serial No. = 0 (If not used) | 0 | 0 | 0000 0000 | |
| 13 | 0D | LCD Module Serial No. = 0 (If not used) | 0 | 0 | 0000 0000 | |
| 14 | 0E | LCD Module Serial No. = 0 (If not used) | 0 | 0 | 0000 0000 | |
| 15 | 0F | LCD Module Serial No. = 0 (If not used) | 0 | 0 | 0000 0000 | |
| 16 | 10 | Week of Manufacture = 25 | 1 | 9 | 0001 1001 | |
| 17 | 11 | Year of Manufacture = 2008 | 1 | 2 | 0001 0010 | |
| 18 | 12 | EDID Structure version # = 1 | 0 | 1 | 0000 0001 | EDID Version/ Revision |
| 19 | 13 | EDID Revision # = 3 | 0 | 3 | 0000 0011 | |
| 20 | 14 | Video Input Definition = Digital I/P,non TMDS CRGB | 8 | 0 | 1000 0000 | Display Parameter |
| 21 | 15 | Max H image size(cm)=28.608cm(29) | 1 | D | 0001 1101 | |
| 22 | 16 | Max V image size(cm)=17.880cm(18) | 1 | 2 | 0001 0010 | |
| 23 | 17 | Display gamma =2.2 | 7 | 8 | 0111 1000 | |
| 24 | 18 | Feature support(DPMS) = Active off, RGB Color | 0 | A | 0000 1010 | |
| 25 | 19 | Red/Green low Bits | 0 | 0 | 0000 0000 | Color Characteristic |
| 26 | 1A | Blue/White Low Bits | 0 | 5 | 0000 0101 | |
| 27 | 1B | Red X = | 0 | 0 | 0000 0000 | |
| 28 | 1C | Red Y = | 0 | 0 | 0000 0000 | |
| 29 | 1D | Green X = | 0 | 0 | 0000 0000 | |
| 30 | 1E | Green Y = | 0 | 0 | 0000 0000 | |
| 31 | 1F | Blue X = | 0 | 0 | 0000 0000 | |
| 32 | 20 | Blue Y = | 0 | 0 | 0000 0000 | |
| 33 | 21 | White X = 0.313 | 5 | 0 | 0101 0000 | |
| 34 | 22 | White Y = 0.329 | 5 | 4 | 0101 0100 | |
| 35 | 23 | Established Timing I = 00h(If not used) | 0 | 0 | 0000 0000 | Established Timings |
| 36 | 24 | Established Timing II = 00h(If not used) | 0 | 0 | 0000 0000 | |
| 37 | 25 | Manufacturer's Timings = 00h(If not used) | 0 | 0 | 0000 0000 | |
| 38 | 26 | Standard Timing Identification 1 was not used | 0 | 1 | 0000 0001 | Standard Timing ID |
| 39 | 27 | Standard Timing Identification 1 was not used | 0 | 1 | 0000 0001 | |
| 40 | 28 | Standard Timing Identification 2 was not used | 0 | 1 | 0000 0001 | |
| 41 | 29 | Standard Timing Identification 2 was not used | 0 | 1 | 0000 0001 | |
| 42 | 2A | Standard Timing Identification 3 was not used | 0 | 1 | 0000 0001 | |
| 43 | 2B | Standard Timing Identification 3 was not used | 0 | 1 | 0000 0001 | |
| 44 | 2C | Standard Timing Identification 4 was not used | 0 | 1 | 0000 0001 | |
| 45 | 2D | Standard Timing Identification 4 was not used | 0 | 1 | 0000 0001 | |
| 46 | 2E | Standard Timing Identification 5 was not used | 0 | 1 | 0000 0001 | |
| 47 | 2F | Standard Timing Identification 5 was not used | 0 | 1 | 0000 0001 | |
| 48 | 30 | Standard Timing Identification 6 was not used | 0 | 1 | 0000 0001 | |
| 49 | 31 | Standard Timing Identification 6 was not used | 0 | 1 | 0000 0001 | |
| 50 | 32 | Standard Timing Identification 7 was not used | 0 | 1 | 0000 0001 | |
| 51 | 33 | Standard Timing Identification 7 was not used | 0 | 1 | 0000 0001 | |
| 52 | 34 | Standard Timing Identification 8 was not used | 0 | 1 | 0000 0001 | |
| 53 | 35 | Standard Timing Identification 8 was not used | 0 | 1 | 0000 0001 | |

## APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

| Byte# (decimal) | Byte# (HEX) | Field Name and Comments | Value (HEX) | | Value (binary) | |
|---|---|---|---|---|---|---|
| 54 | 36 | 1280 X 800 @ 60Hz mode : pixel clock = 69.3MHz | 1 | 2 | 0001 0010 | Detailed Timing Description #1 |
| 55 | 37 | (Stored LSB first) | 1 | B | 0001 1011 | |
| 56 | 38 | Horizontal Active = 1280 pixels | 0 | 0 | 0000 0000 | |
| 57 | 39 | Horizontal Blanking = 128 pixels | 8 | 0 | 1000 0000 | |
| 58 | 3A | Horizontal Active : Horizontal Blanking = 1280 : 128 | 5 | 0 | 0101 0000 | |
| 59 | 3B | Vertical Avtive = 800 lines | 2 | 0 | 0010 0000 | |
| 60 | 3C | Vertical Blanking = 16 lines | 1 | 0 | 0001 0000 | |
| 61 | 3D | Vertical Active : Vertical Blanking = 800 : 16 | 3 | 0 | 0011 0000 | |
| 62 | 3E | Horizontal Sync. Offset = 24 pixels | 1 | 8 | 0001 1000 | |
| 63 | 3F | Horizontal Sync Pulse Width = 32 pixels | 2 | 0 | 0010 0000 | |
| 64 | 40 | Vertical Sync Offset = 4 lines, Sync Width = 4 lines | 4 | 4 | 0100 0100 | |
| 65 | 41 | Horizontal Vertical Sync Offset/Width upper 2bits = 0 | 0 | 0 | 0000 0000 | |
| 66 | 42 | Horizontal Image Size = 331.2mm(331) | 4 | B | 0100 1011 | |
| 67 | 43 | Vertical Image Size = 207.0mm(207) | C | F | 1100 1111 | |
| 68 | 44 | Horizontal & Vertical Image Size | 1 | 0 | 0001 0000 | |
| 69 | 45 | Horizontal Border = 0 | 0 | 0 | 0000 0000 | |
| 70 | 46 | Vertical Border = 0 | 0 | 0 | 0000 0000 | |
| 71 | 47 | Non-interlaced,Normal display,no stereo,Digital separate sync,H/V pol negatives | 1 | 8 | 0001 1000 | |
| 72 | 48 | Detailed Timing Descriptor #2 | 0 | 0 | 0000 0000 | Detailed Timing Description #2 |
| 73 | 49 | | 0 | 0 | 0000 0000 | |
| 74 | 4A | | 0 | 0 | 0000 0000 | |
| 75 | 4B | | 0 | 0 | 0000 0000 | |
| 76 | 4C | | 0 | 0 | 0000 0000 | |
| 77 | 4D | | 0 | 0 | 0000 0000 | |
| 78 | 4E | | 0 | 0 | 0000 0000 | |
| 79 | 4F | | 0 | 0 | 0000 0000 | |
| 80 | 50 | | 0 | 0 | 0000 0000 | |
| 81 | 51 | | 0 | 0 | 0000 0000 | |
| 82 | 52 | | 0 | 0 | 0000 0000 | |
| 83 | 53 | | 0 | 0 | 0000 0000 | |
| 84 | 55 | | 0 | 0 | 0000 0000 | |
| 85 | 55 | | 0 | 0 | 0000 0000 | |
| 86 | 56 | | 0 | 0 | 0000 0000 | |
| 87 | 57 | | 0 | 0 | 0000 0000 | |
| 88 | 58 | | 0 | 0 | 0000 0000 | |
| 89 | 59 | | 0 | 0 | 0000 0000 | |
| 90 | 5A | Detailed Timing Descriptor #3 | 0 | 0 | 0000 0000 | Detailed Timing Description #3 |
| 91 | 5B | | 0 | 0 | 0000 0000 | |
| 92 | 5C | | 0 | 0 | 0000 0000 | |
| 93 | 5D | | F | E | 1111 1110 | |
| 94 | 5E | | 0 | 0 | 0000 0000 | |
| 95 | 5F | | 0 | 0 | 0000 0000 | |
| 96 | 60 | | 0 | 0 | 0000 0000 | |
| 97 | 61 | | 0 | 0 | 0000 0000 | |
| 98 | 62 | L | 4 | C | 0100 1100 | |
| 99 | 63 | G | 4 | 7 | 0100 0111 | |
| 100 | 64 | D | 4 | 4 | 0100 0100 | |
| 101 | 65 | I | 4 | 9 | 0100 1001 | |
| 102 | 66 | S | 5 | 3 | 0101 0011 | |
| 103 | 67 | P | 5 | 0 | 0101 0000 | |
| 104 | 68 | L | 4 | C | 0100 1100 | |
| 105 | 69 | A | 4 | 1 | 0100 0001 | |
| 106 | 6A | Y | 5 | 9 | 0101 1001 | |
| 107 | 6B | LF | 0 | A | 0000 1010 | |

## APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

| Byte# (decimal) | Byte# (HEX) | Field Name and Comments | Value (HEX) | Value (binary) | |
|---|---|---|---|---|---|
| 108 | 6C | Detailed Timing Descriptor #4 | 0 0 | 0000 0000 | |
| 109 | 6D | | 0 0 | 0000 0000 | |
| 110 | 6E | | 0 0 | 0000 0000 | |
| 111 | 6F | | F E | 1111 1110 | |
| 112 | 70 | | 0 0 | 0000 0000 | |
| 113 | 71 | L | 4 C | 0100 1100 | |
| 114 | 72 | P | 5 0 | 0101 0000 | **Detailed Timing Description #4** |
| 115 | 73 | 1 | 3 1 | 0011 0001 | |
| 116 | 74 | 3 | 3 3 | 0011 0011 | |
| 117 | 75 | 3 | 3 3 | 0011 0011 | |
| 118 | 76 | W | 5 7 | 0101 0111 | |
| 119 | 77 | X | 5 8 | 0101 1000 | |
| 120 | 78 | 2 | 3 2 | 0011 0010 | |
| 121 | 79 | - | 2 D | 0010 1101 | |
| 122 | 7A | T | 5 4 | 0101 0100 | |
| 123 | 7B | L | 4 C | 0100 1100 | |
| 124 | 7C | E | 4 5 | 0100 0101 | |
| 125 | 7D | 1 | 3 1 | 0011 0001 | |
| 126 | 7E | Extension flag = 00 | 0 0 | 0000 0000 | **Extension Flag** |
| 127 | 7F | Checksum | **4 F** | **0100 1111** | **Checksum** |